615-73, Rev. B

SOFTWARE REQUIREMENTS DOCUMENT
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EXPERIMENT DATA RECORDS
GENERATION
— EDRGEN —

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Table 5.1.2.2-B2.   MAG Data Record Header Format (contd)

Item	Word No.	Bits*	Title/Function
		24	GCF block error flag status. 0 = No errors, 1 = Error flag was set.
		25	Not used.
		26	Complete frame flag. Indicates if any of the words in the data block of this record do not contain valid data. 0 = Complete frame, 1 = Date missing.
		27-31	Not used.
8	20	0-31	First DSIF Station Code Station which received data for first NIS frame in first physical record of logical record

Valve	Sta	Valve	Sta
0	27	8	63
1	51	9	43
2	14	10	41
3	71	11	61
4	11	12	12
5	42	13	Other
6	21	14	Other
7	62	15	Other

*All words (32 bits) or half-words are right-adjusted; i.e., values are extended with leading binary zeros on the left. MSB is bit zero for full words and bits zero and 16 for half-words. Leftmost bit is bit zero.

Table 5.1.2.2-B2. MAG Data Record Header Format (contd)

Item	Word No.	Bits*	Title/Function
9	21	0-31	Last DSIF Station Code Station which received data for the first NIS frame in last physical record of logical record (see table in Item 8)
10		0-31	SNR SNR value from header of first NIS frame used to generate a physical record. Value in two's complement with binary point between Units = dB
	22 thru 28	0-31	One from each of the 7-physical records/logical record of the MTC tape.
11			Composite Bit Errors Number of errors detected in 10^5 sync word bits from header of first NIS frame used to generate a physical record.
			Missing Segment Indicator Indicates a missing segment of logical record. Data quality flags in header and data flag with each piece of data will be set to zero for missing segments.
	29 thru 35	0-31	One from each of the 7 physical records/logical record of the MTC tape. A value of $FFFFFFFF_{16}$ indicates a missing segment. A positive value is a composite bit error reading.
12			DSIF Configuration Latest available configuration status
	36	18-31	Configuration from first physical record of logical record

Bits 18-20

001	=	RCVR 1
010	=	RCVR 2
011	=	RCVR 3
100	=	RCVR 4
101	=	RCVR 5
110	=	RCVR 6

Bits 21-23

000	=	SDA 8
001	=	SDA 1
010	=	SDA 2
011	=	SDA 3
100	=	SDA 4
101	=	SDA 5
110	=	SDA 6
111	=	SDA 7

*All words (32 bits) or half-words are right-adjusted; i.e., values are extended with leading binary zeros on the left. MSB is bit zero for full words and bits zero and 16 for half-words. Leftmost bit is bit zero.

Change 1, 6/8/73